



<b>INFORMATION DISCLOSURE CITATION PTO-1449</b>	Attachment No. <b>010704</b>	Serial No. <b>09/871,810</b>
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**U.S. PATENT DOCUMENTS**

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					
_____	AE					

**FOREIGN PATENT DOCUMENTS**

Document No.	Date	Country	Translation (Yes or No)
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**OTHER DOCUMENTS**

<u>Cel</u>	AH	Patterson et al., "Structure and Design of a Computer, by David A. Patterson, and John L. Hennessy, published by NikkeiBP, September 22, 1997, page 677.
<u>Cel</u>	AI	Technical Report of Low Power LSI, Nikkei Micro-device, NikkeiBP, 1994, page 90.
<u>Cel</u>	AJ	"A Low Power Multiplier Using Adiabatic Charging Binary Decision Diagram Circuit", <u>Extended Abstracts of the 1999 International Conference on Solid State Devices and Materials</u> , Tokyo, 1999, pages 444-445.
<u>Cel</u>	AK	Svensson et al., "Low Power Circuit Techniques", <u>Low Power Design Methodologies</u> , Kluwer Academic Publishers, 1996, Chapter 3, pages 37-52.
Examiner <u>Cassandra Cox</u>	Date Considered	<u>01/09/03</u>